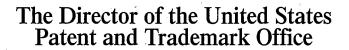
The United States of America



Has received an application for a patent for a new and useful invention. The title and description of the invention are enclosed. The requirements of law have been complied with, and it has been determined that a patent on the invention shall be granted under the law.

Therefore, this

United States Patent

Grants to the person(s) having title to this patent the right to exclude others from making, using, offering for sale, or selling the invention throughout the United States of America or importing the invention into the United States of America, and if the invention is a process, of the right to exclude others from using, offering for sale or selling throughout the United States of America, or importing into the United States of America, products made by that process, for the term set forth in 35 U.S.C. 154(a)(2)or (c)(1), subject to the payment of maintenance fees as provided by 35 U.S.C. 41(b). See the Maintenance Fee Notice on the inside of the cover.

David J. Kappas

Director of the United States Patent and Trademark Office



US008164883B2

(12) United States Patent Chiu et al.

(10) Patent No.:

US 8,164,883 B2

(45) Date of Patent:

Apr. 24, 2012

(54) STACKED SOLID ELECTROLYTIC CAPACITOR AND A METHOD FOR MANUFACTURING THE SAME

Inventors: Chi-Hao Chiu, Hsinchu (TW);

Ching-Feng Lin, Zhudong Township, Hsinchu County (TW); Chun-Chia Huang, Xinying (TW); Chien-Ting Liu,

Taipei (TW)

(73) Assignee: Apaq Technology Co., Ltd., Miaoli

County (TW)

(*) Notice: Subject to any disclaimer, the term of this

patent is extended or adjusted under 35

U.S.C. 154(b) by 176 days.

(21) Appl. No.: 12/813,980

(22) Filed: Jun. 11, 2010

(65) Prior Publication Data

US 2011/0122544 A1 May 26, 2011

(30) Foreign Application Priority Data

Nov. 23, 2009 (TW) 98139713 A

(51) Int. Cl.

H01G 9/00 (2006.01)

See application file for complete search history.

(56) References Cited

U.S. PATENT DOCUMENTS

5,377,073 A * 6,249,424 B1 * 6,563,693 B2 * 6,977,807 B2 * 7,206,193 B2 * 7,417,844 B2 *	6/2001 5/2003 12/2005 4/2007	Fukaumi et al	361/523 361/523 361/523 361/535
--	---------------------------------------	---------------	--

* cited by examiner

Primary Examiner - Nguyen T Ha

(74) Attorney, Agent, or Firm — Muncy, Geissler, Olds & Lowe, PLLC

(57) ABSTRACT

A stacked solid electrolytic capacitor and a method for manufacturing the same are disclosed. The stacked solid electrolytic capacitor includes two capacitor sets, a positive electrode conducting device, a negative electrode conducting device, and a package unit. Each capacitor set includes at least one capacitor unit. The front side of the positive electrode portion of the capacitor set extends to form a positive electrode pin. The positive electrode conducting device has at least one first positive electrode conducting lead frame and at least one second positive electrode conducting lead frame. The first positive electrode conducting lead frame is electrically connected with the second positive electrode conducting lead frame. The negative electrode conducting device has at least one negative electrode conducting lead frame, and is electrically connected with the negative electrode of the two capacitor sets by using metal conductive material.

28 Claims, 5 Drawing Sheets

